

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Stefan HÖRETH ET AL.
 Appln. No.: Not yet assigned
 Filed: Herewith
 For: PROCESS AND DEVICE FOR
 THE VERIFICATION OF
 DIGITAL CIRCUITS
 Attorney Docket No: 10808/193

Examiner: Not yet assigned

Art Unit: N/A

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98, and more particularly in accordance with 37 C.F.R. §1.97(b), Applicants hereby cite the following reference(s):

No.	Date of Publication	Patentee/Applicant/Assignee
6,026,222	February 15, 2000	Gupta et al.
DOCUMENT NUMBER	DATE	COUNTRY
WO 2004/025520 A3	08/19/2003	WO
OTHER ART – NON PATENT LITERATURE DOCUMENTS		
Copy of International Examination Report from corresponding PCT application number PCT/EP03/09179.		
Dominick Stoffel and Wolfgang Kunz, <i>Verification of Integer Multipliers on the Arithmetic Bit Level</i> , pgs. 183-189, IEEE, 2001.		
Ying-Tsai Chang and Kwang-Ting (Tim) Cheng, <i>Introduction-Based Gate-Level Verification of Multipliers</i> , pgs. 190-193, IEEE, 2001.		
Jiunn-Chern Chen and Yirng-An Chen, <i>Equivalence Checking of Integer Multipliers</i> , IEEE, 2001.		
Chien-Chung Tsai and Malgorzata Marek-Sadowska, <i>Boolean Matching Using Generalized Reed-Muller Forms</i> , published prior to the filing of this application.		

Applicants are enclosing Form PTO-1449 (one sheet), along with a copy of each listed reference for which a copy is required under 37 C.F.R. §1.98(a)(2). As each of the listed references is in English, no further commentary is believed to be necessary, 37 C.F.R. §1.98(a)(3). Applicants respectfully request the Examiner's consideration of the above reference(s) and entry thereof into the record of this application.

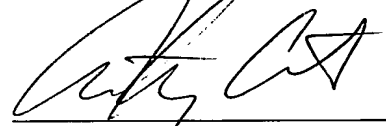
By submitting this Statement, Applicants are attempting to fully comply with the duty of candor and good faith mandated by 37 C.F.R. §1.56. As such, this

Statement is not intended to constitute an admission that any of the enclosed references, or other information referred to therein, constitutes "prior art" or is otherwise "material to patentability," as that phrase is defined in 37 C.F.R. §1.56(a).

Applicants have calculated no fee to be due in connection with the filing of this Statement. However, the Director is authorized to charge any fee deficiency associated with the filing of this Statement to a deposit account, as authorized in the Transmittal accompanying this Statement.

2/28/05
Date

Respectfully submitted,



Anthony P. Curtis, Ph.D.
(Reg. No. 46,193)

28 NOV 2005

10/525999

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Stefan HÖRETH ET AL.

Appln. No.: 10/525,999

Filed: February 28, 2005

For: PROCESS AND DEVICE FOR
THE VERIFICATION OF
DIGITAL CIRCUITS

Examiner: Not yet assigned

Art Unit: N/A

Attorney Docket No: 12951/7

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and §§1.97-1.98, and more particularly in accordance with 37 C.F.R. §1.97(b), Applicant hereby cites the following reference(s):

OTHER ART – NON PATENT LITERATURE DOCUMENTS

Copy of International Preliminary Examination Report from corresponding PCT application number PCT/EP03/09179.

Applicant is enclosing Form PTO-1449 (one sheet), along with a copy of each listed reference for which a copy is required under 37 C.F.R. §1.98(a)(2). As each of the listed references is in English, no further commentary is believed to be necessary, 37 C.F.R. §1.98(a)(3). Applicant respectfully requests the Examiner's consideration of the above reference(s) and entry thereof into the record of this application.

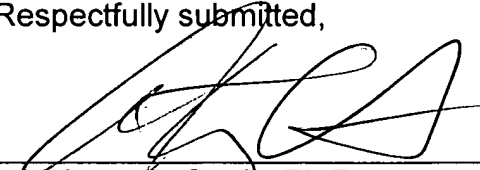
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with the filing of this Statement to a deposit account, as authorized in the Transmittal accompanying this Statement.

Respectfully submitted,

11/23/05
Date



Anthony P. Curtis, Ph.D.
(Reg. No. 46,193)

FORM PTO-1449	SERIAL NO. 10/525,999	CASE NO. 12951/7
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE February 28, 2005	GROUP ART UNIT Not yet assigned
(use several sheets if necessary)	APPLICANT(S): Stefan HÖRETH ET AL.	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO

EXAMINER INITIAL	OTHER ART – NON PATENT LITERATURE DOCUMENTS <small>(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)</small>	
	B1	Copy of International Preliminary Examination Report from corresponding PCT application number PCT/EP03/09179.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

10/525999

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	A5	Ying-Tsai Chang and Kwang-Ting (Tim) Cheng, <i>Introduction-Based Gate-Level Verification of Multipliers</i> , pgs. 190-193, IEEE, 2001.
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